# **Harrison York**

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#### **EDUCATION**

**Duke University** May 2026

BSE, Electrical & Computer Engineering and Computer Science

Durham, NC

**GPA:** 4.00

Dean's List with Distinction, Society of Hispanic Professional Engineers (SHPE)

Coursework: Operating Systems, Network Architecture, Microelectronics, Robotics & Automation, Probability for Machine Learning, Computer Architecture, Digital Systems, Data Structures & Algorithms, Signals & Systems

#### **EXPERIENCE**

# **SpaceX - Software Engineering Intern**

May 2025 - Aug 2025

Hawthorne, CA

Starlink Product Engineering

- Developed full-stack internal app to monitor Starlink User Terminal testbeds, focusing on caching, load times, automated deployment, and network impact with software including Ansible, Redis, Flask, Docker, and React
- Upgraded automated testing infrastructure with enterprise servers and VM clusters to increase HITL reliability and decrease time to release
- Owned projects from start to completion, including leading design discussions and coordinating across teams to drive progress

## Microsoft - Software Engineering Intern

May 2024 - Jul 2024

**Power Pages** 

Redmond, WA

- Streamlined process for adding custom web components to sites, increasing user efficiency by 80%
- Collaborated with PM and design teams to collect user feedback and enhance project implementation
- Deployed new UI/UX features to increase product accessibility using React and TypeScript

## **Amazon - Software Engineering Intern**

Jun 2023 – Aug 2023

AWS Lambda

Seattle, WA

- Completed a proof-of-concept estimated to reduce operating costs by over \$1 million annually
- Implemented object-oriented programs and wrote corresponding unit tests with >90% coverage
- Delivered detailed design docs and presentations to stakeholders

### **Duke Motorsports - Electrical Systems Engineer**

Aug 2023 - Present

Durham, NC

Formula SAE team member

- Improved electrical harness with new sensors and routing with RapidHarness
- Designed PCB for steering wheel upgrade using Altium, prototyped using Arduino
- Calibrated sensors, installed electrical harness, troubleshooted vehicle system failures

#### **PROJECTS**

## **Processor and Robot Arm**

Apr 2024 - May 2024

- Built 5-stage pipelined processor in Verilog to implement on an FPGA using Vivado, supporting reduced MIPS ISA
- Integrated processor with 3D-printed robot arm using joystick inputs
- Created custom prints (Fusion 360), circuits, and control signals (PWM)

#### **SKILLS**

Java, Python, C, JavaScript, React, Verilog, FPGA, HTML, CSS, MATLAB, Assembly, Altium

## **AWARDS**

### **Lockheed Martin STEM Scholar**

2023